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# Weiterentwicklung

## High Performance Software Defined Radio



Realisierung gemeinsam mit der  
Tucson Amateur Packet Radio Corp.  
unter einer 'Open Hardware License'

# High Performance Software Defined Radio

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The HPSSDR is an open source (GNU type) hardware and software project intended as a "next generation" Software Defined Radio (SDR) for use by Radio Amateurs ("hams") and Short Wave Listeners (SWLs).

It is being designed and developed by a group of SDR enthusiasts with representation from interested experimenters worldwide.

<http://hpsdr.org/>



# HPSDR Module

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## ATLAS - Backplane

The Atlas is a passive backplane that all other modules plug into.

## JANUS - ADC/DAC Board

The Janus module is a very high performance, dual, full duplex, A/D and D/A converter board.

## OZY - HPSDR Host Interface & Control

The OZY module is an FPGA based interface controller card that provides the input and output connections to the real world.

## MERCURY - 0-30MHz Direct Sampling Receiver

Perhaps the most exciting of all the modules, the Mercury board will enable direct sampling of the 0-65MHz spectrum.

## SASQUATCH - DSP back-end

The Sasquatch board is a hardware DSP back-end intended for use by constructors who would like to operate the HPSDR stand-alone rather than attached to a PC.

## GIBRALTAR - GPS-disciplined Frequency Standard

GIBRALTAR is a GPS-disciplined frequency standard board.

## PROTEUS - Prototyping Board

This is the planned prototyping board.

## HORTON - Receiver Module

A receiver module integrating the Janus ADC with a QSD on a board for a version of the HPSDR RX board.

## PINOCCHIO - Extender Card

Pinocchio is an extender card to allow measurements and troubleshooting of an active card in an ATLAS backplane.

## CASMIR - Transmitter Card

This is a pair of transmitter cards for the HPSDR platform.

## PHOENIX - QSD/QSE Receiver/Transmitter Module

QSD based HF Receiver, a QSE based HF Exciter and a supporting synthesizer.

## ODYSSEY - Low Power Handheld SDR

Odyssey includes a low power SDR based on the QSD, QSE, and a dsPIC33 as the basic radio core.